. (<u>G</u>

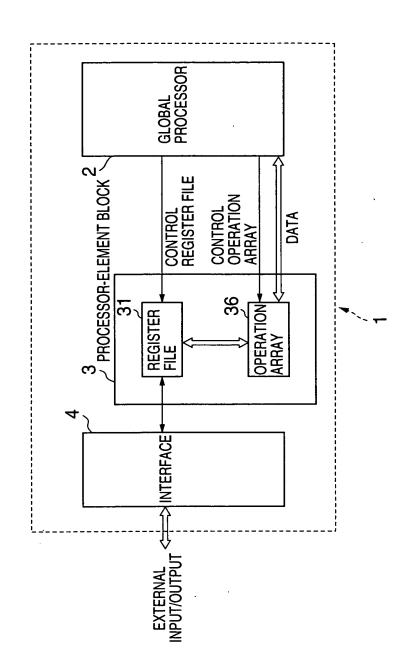
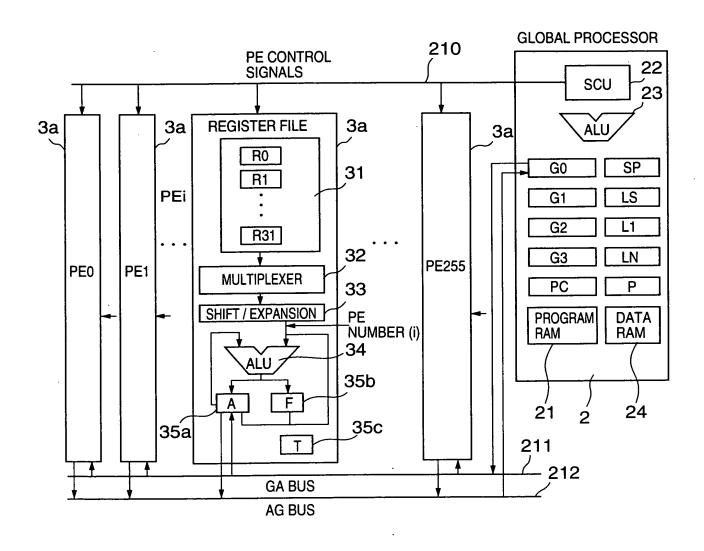


FIG. 2



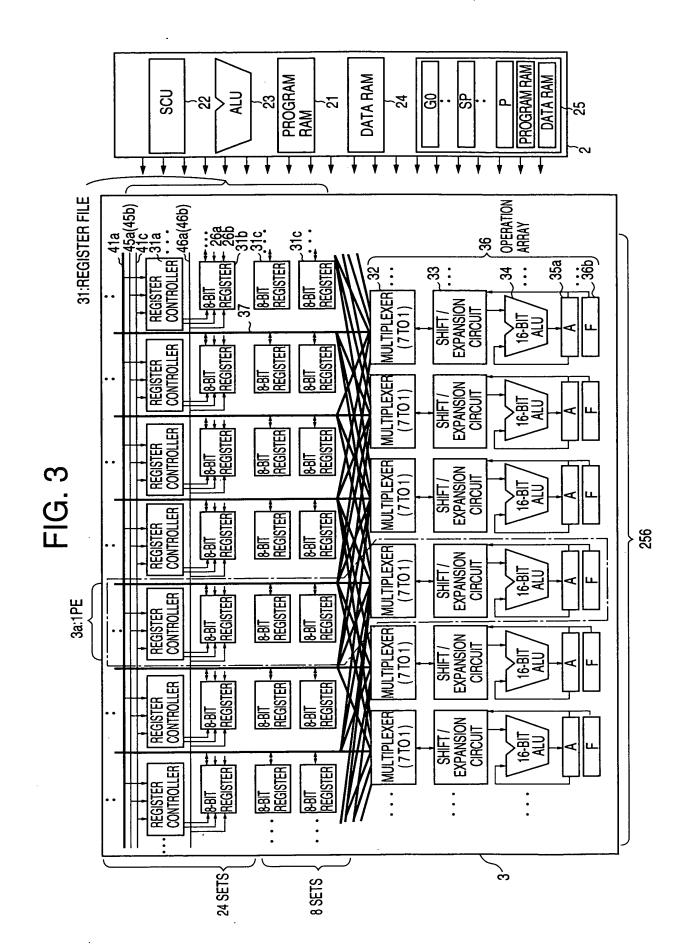


FIG. 4

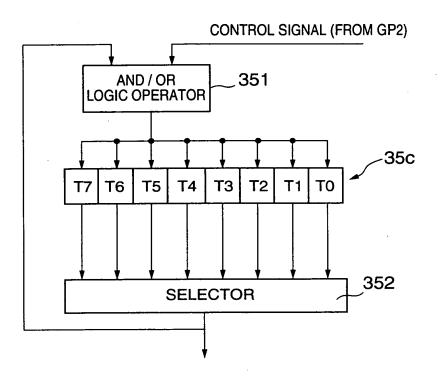


FIG. 5

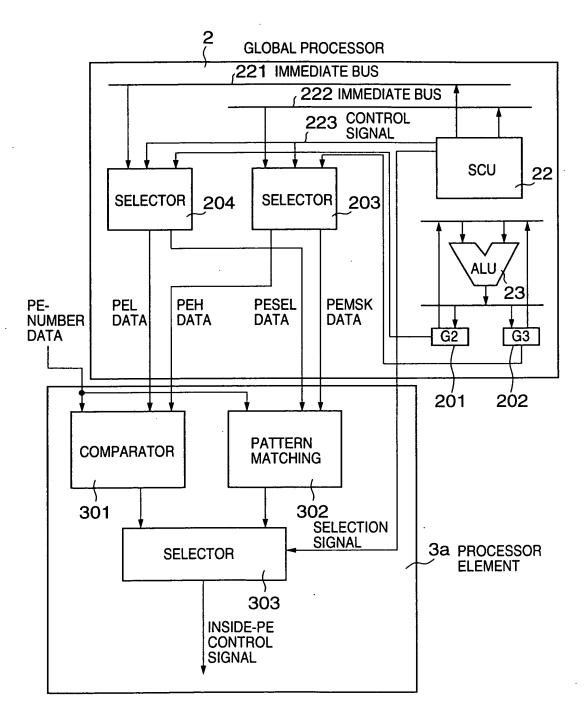


FIG. 6

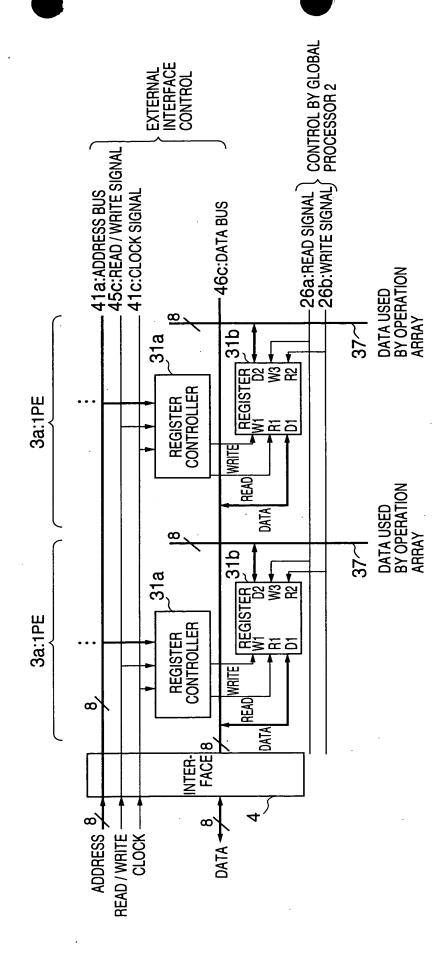


FIG. 7

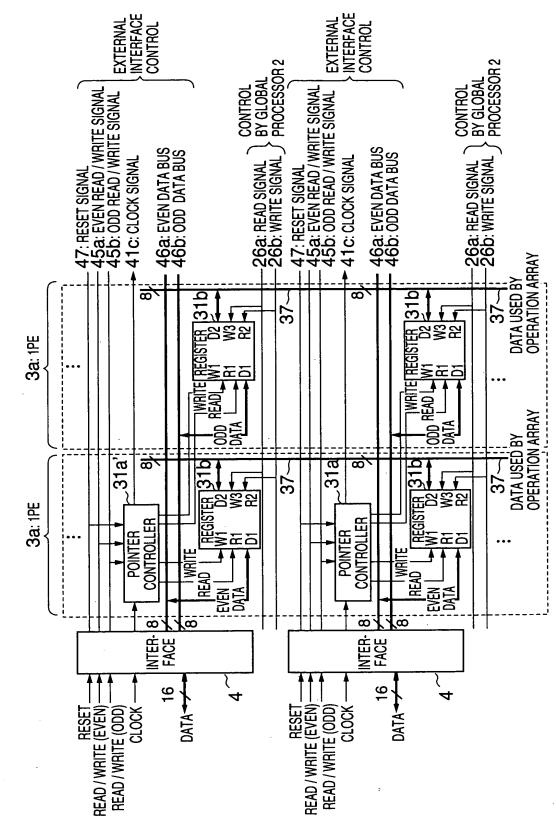
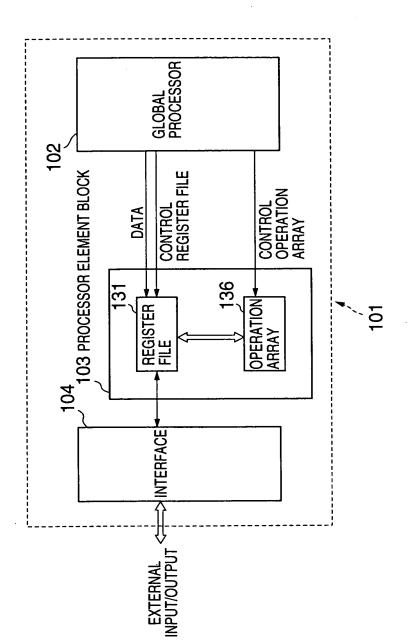
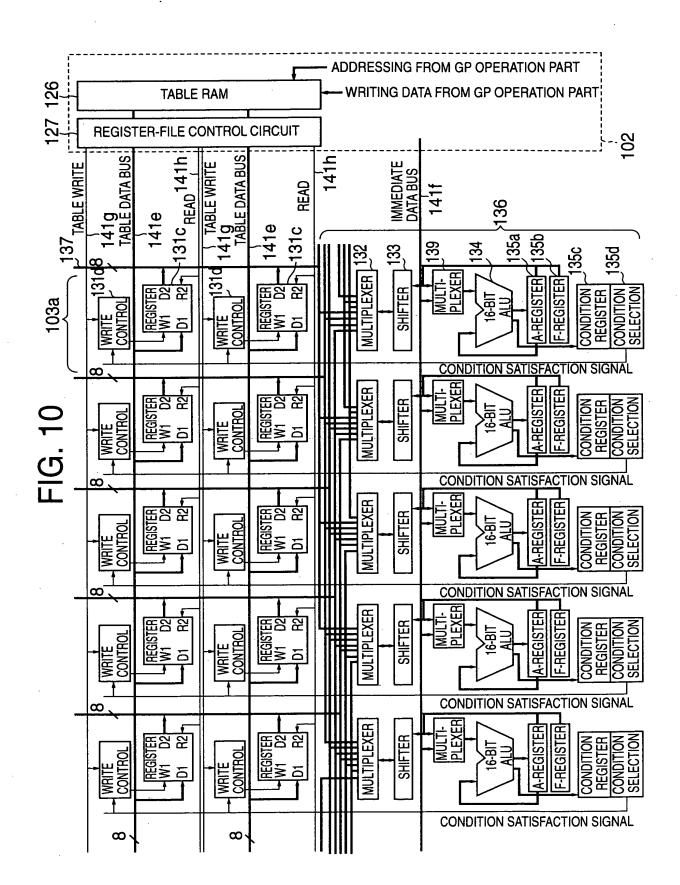


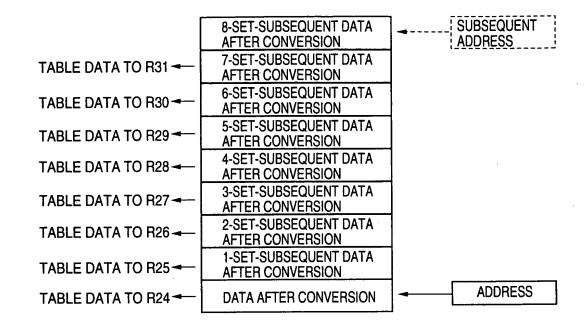
FIG. 8



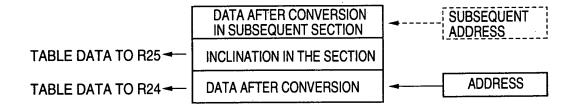
PROGRAM RAM DATA RAM 125 122 124 <u>8</u> SCU 12 SP 9 Ь 131:REGISTER FILE OPERATION ARRAY 136 137 8-817 131c 141a 131a 131a 131a 7336 1,141d REGISTER CONTROLLER -135a .135b 137 8-BIT REGISTER 8-BIT REGISTER . <del>1</del>33 134 MULTIPLEXER (7 TO 1) EXPANSION CIRCUIT ALU ALU SHF1 REGISTER CONTROLLER 8-BIT REGISTER | FBBIT | FEGISTER 8-BIT REGISTER MULTIPLEXER (7 TO 1) EXPANSION CIRCUIT 16-BIT ALU L L L S 1 LEBIT REGISTER REGISTER CONTROLLER 8-BIT REGISTER 8-BIT REGISTER FIG. 9 MULTIPLEXER (7 TO 1) EXPANSION CIRCUIT 16-BIT ALU SHF CONTROLLER REGISTER 8-BIT REGISTER 8-BIT REGISTER 256PE REGISTER MULTIPLEXER (7 TO 1) EXPANSION CIRCUIT AEU, SHIFT / 103a:1PE CONTROLLER 8-BIT REGISTER REGISTER 8-BIT REGISTER REGISTER ||-#8H| MULTIPLEXER SHIFT / EXPANSION CIRCUIT (7701)16-BIT ALU register Controller L BBIT REGISTER 8-BIT REGISTER 8-BIT REGISTER MULTIPLEXER SHIFT / EXPANSION CIRCUIT (7701)16-BIT ALU I CEBIT REGISTER REGISTER CONTROLLER 8-BIT REGISTER 8-BIT REGISTER 141e 141e 141 8 SETS <del>1</del>33 24 SETS NAER. Page <del>1</del>04



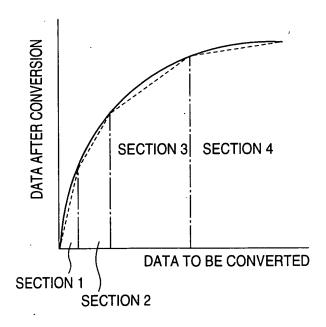
## FIG. 11

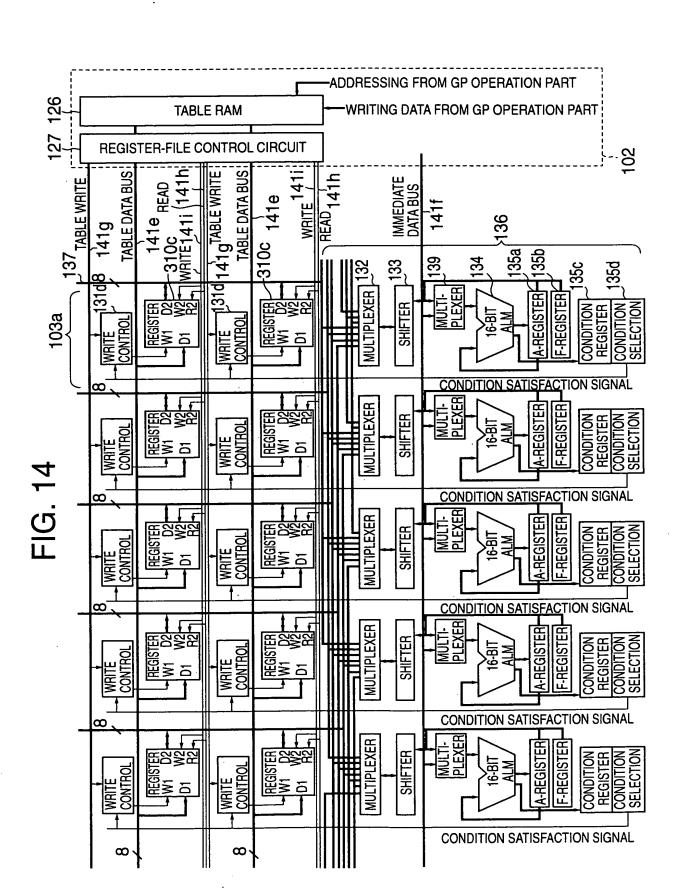


## FIG. 12



## FIG. 13





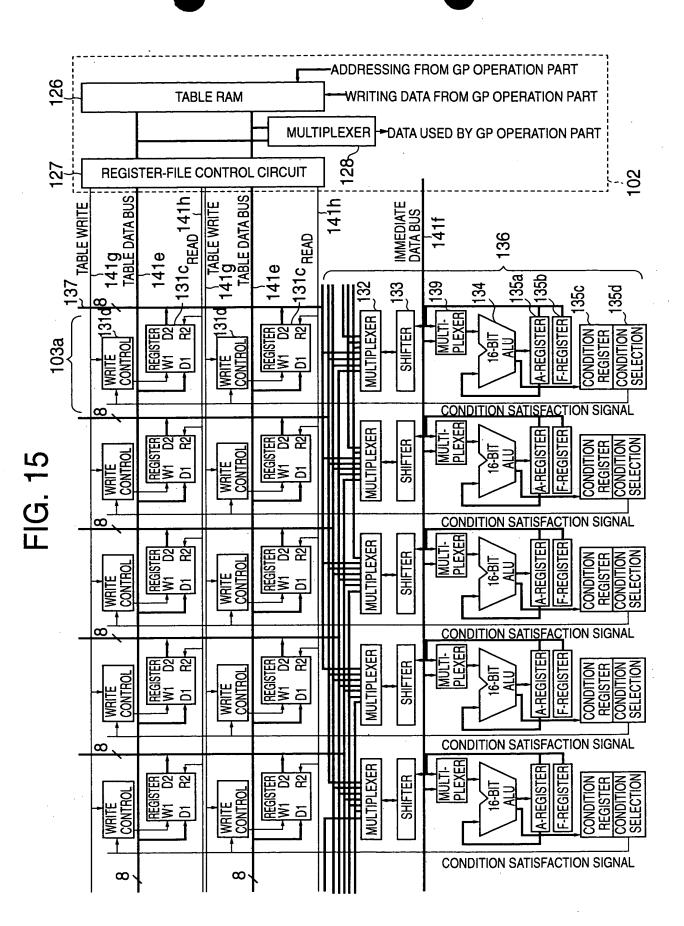


FIG. 16

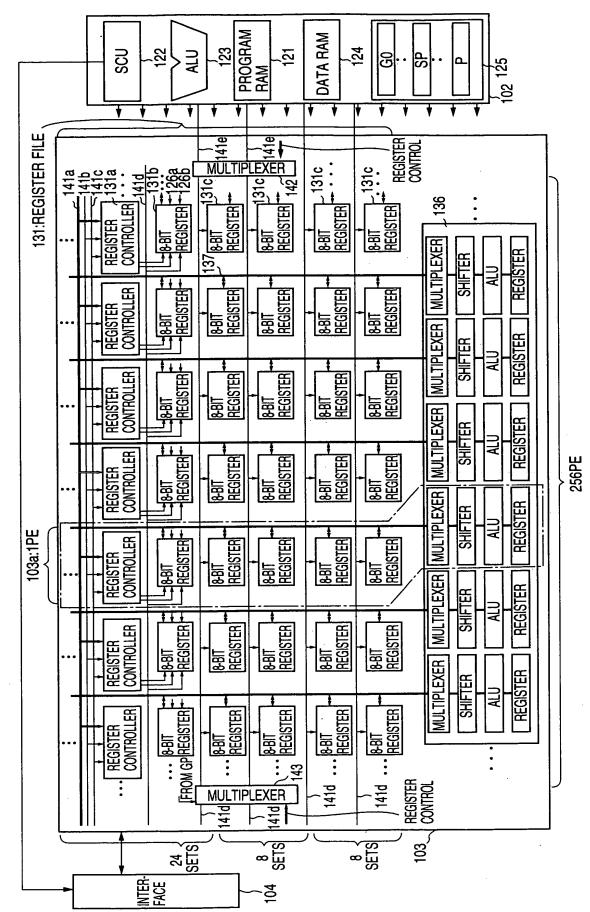


FIG. 17

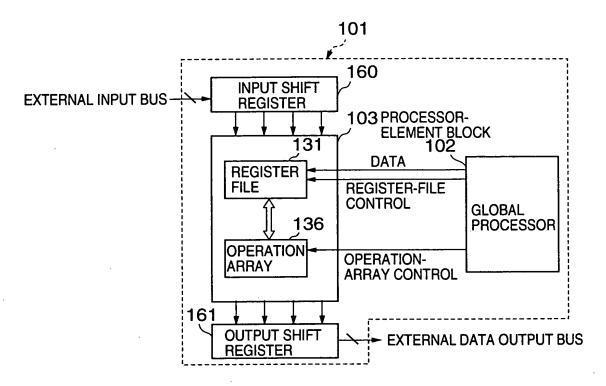


FIG. 18

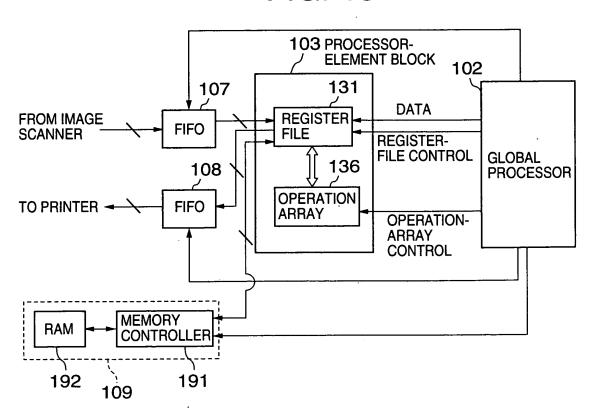


FIG. 19

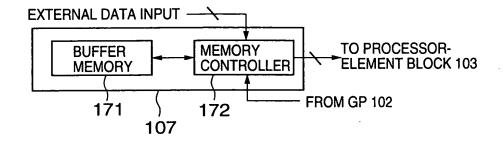


FIG. 20

